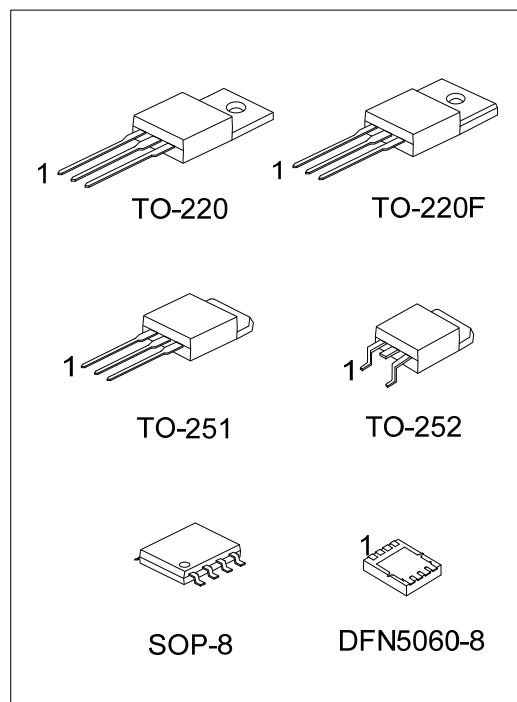
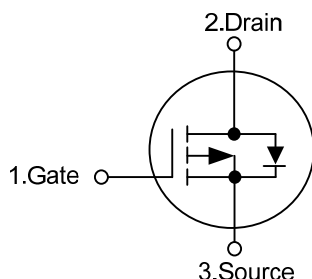


**UT70P03****Power MOSFET****-70A, -30V P-CHANNEL
POWER MOSFET****DESCRIPTION**

The **UT70P03** uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * $R_{DS(ON)} \leq 10 \text{ m}\Omega$ @ $V_{GS} = -10\text{V}$, $I_D = -35\text{A}$
- * $R_{DS(ON)} \leq 12 \text{ m}\Omega$ @ $V_{GS} = -4.5\text{V}$, $I_D = -35\text{A}$
- * Low Capacitance
- * Low Gate Charge
- * Fast Switching Capability
- * Avalanche Energy Specified

SYMBOL**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT70P03L-TA3-T	UT70P03G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UT70P03L-TF3-T	UT70P03G-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
UT70P03L-TM3-T	UT70P03G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
UT70P03L-TN3-R	UT70P03G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT70P03L-S08-R	UT70P03G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel
UT70P03L-K08-5060-R	UT70P03G-K08-5060-R	DFN5060-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UT70P03G-TA3-T 		(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TM3: TO-251 TN3: TO-252, S08: SOP-8, K08-5060: DFN5060-8 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

PACKAGE	MARKING
TO-220 / TO-220F TO-251 / TO-252	UTC UT70P03 Lot Code 1 L: Lead Free G: Halogen Free Date Code
SOP-8	8765 UTC UT70P03 1234 Date Code L: Lead Free G: Halogen Free Lot Code
DFN5060-8	UTC UT 70P03 Lot Code Date Code

■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ($T_C=25^{\circ}\text{C}$)	TO-220/TO-220F TO-251/TO-252	I_D	-70	A
	SOP-8		-50	A
	DFN5060-8		-50	A
Pulsed Drain Current(Note 2)	TO-220/TO-220F TO-251/TO-252	I_{DM}	-140	A
	SOP-8		-100	A
	DFN5060-8		-100	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	224.2	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	21.7	V/ns
Power Dissipation ($T_C=25^{\circ}\text{C}$)	TO-220	P_D	147	W
	TO-220F		37	W
	TO-251/ TO-252		65	W
	SOP-8		5	W
	DFN5060-8		13.6	W
Junction Temperature		T_J	+175	$^{\circ}\text{C}$
Strong Temperature		T_{STG}	-55 ~ +175	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 0.1\text{mH}$, $I_{AS} = -66.9\text{A}$, $V_{DD} = -30\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD} \leq -30\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
	TO-251/TO-252		110	
	SOP-8		125 (Note)	
	DFN5060-8		65 (Note)	
Junction to Case	TO-220	θ_{JC}	0.85	$^{\circ}\text{C}/\text{W}$
	TO-220F		3.4	
	TO-251/TO-252		1.92 (Note)	
	SOP-8		25 (Note)	
	DFN5060-8		9.1 (Note)	

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

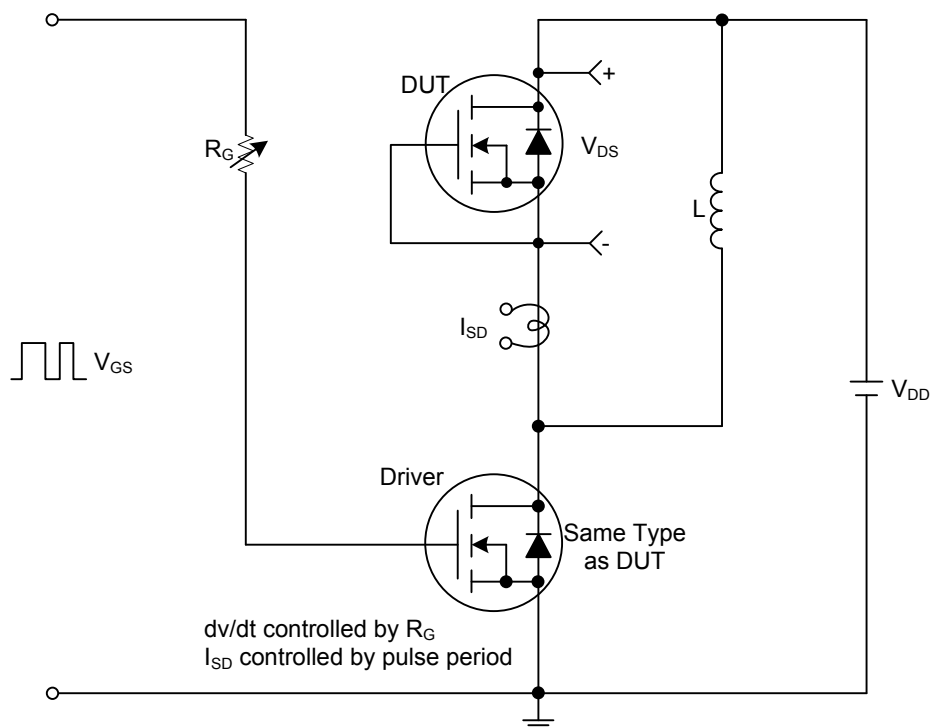
■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250μA	-30			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V, T _J =25°C			-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-Resistance (Note 2)	R _{DS(ON)}	V _{GS} =-10V, I _D =-35A			10	mΩ
		V _{GS} =-4.5V, I _D =-35A			12	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{DS} =-25V, V _{GS} =0V, f=1.0MHz		5500		pF
Output Capacitance	C _{OSS}			911.3		pF
Reverse Transfer Capacitance	C _{RSS}			858.4		pF
SWITCHING PARAMETERS						
Total Gate Charge(Note 2)	Q _G	V _{DS} =-24V, V _{GS} =-10V, I _D =-70A I _G =1mA (Note 1, 2)		119		nC
Gate Source Charge	Q _{GS}			26		nC
Gate Drain ("Miller") Charge	Q _{GD}			24.8		nC
Turn-ON Delay Time(Note 2)	t _{D(ON)}	V _{DS} =-15V, V _{GS} =-10V, I _D =-70A, R _G =3.3Ω (Note 1, 2)		15.8		ns
Turn-ON Rise Time	t _R			19.6		ns
Turn-OFF Delay Time	t _{D(OFF)}			101		ns
Turn-OFF Fall-Time	t _F			67		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				-70	A
Maximum Body-Diode Pulsed Current	I _{SM}				-140	A
Forward On Voltage(Note 2)	V _{SD}	I _S =-70A, V _{GS} =0V			-1.3	V
Reverse Recovery Time	t _{rr}	I _S =-30A, V _{GS} =0V,		150		ns
Reverse Recovery Charge	Q _{rr}	dI/dt=100 A/us		928		nC

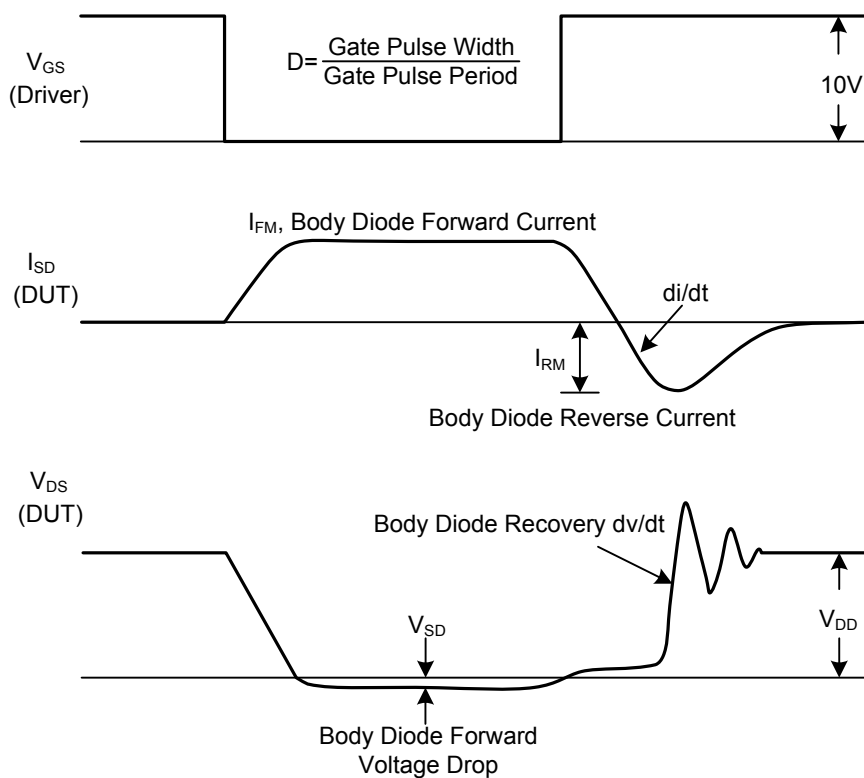
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



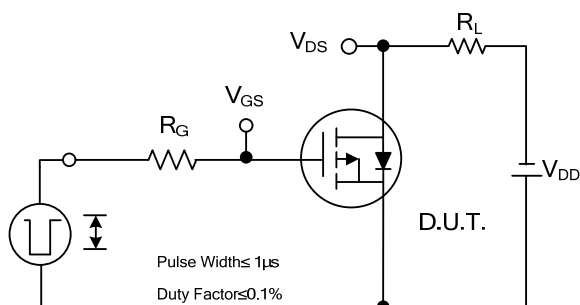
Peak Diode Recovery dv/dt Test Circuit



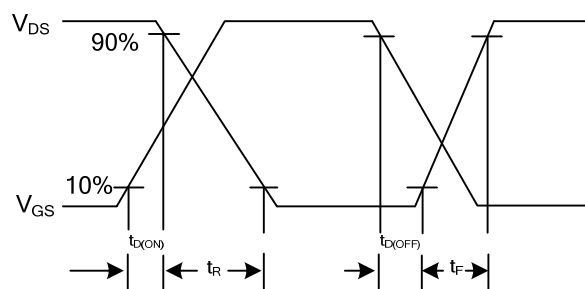
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

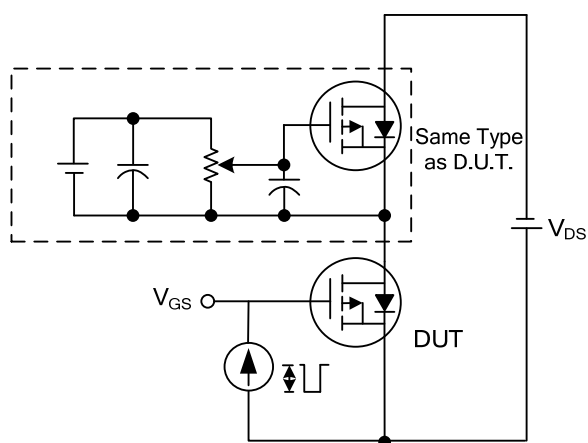
■ TEST CIRCUITS AND WAVEFORMS



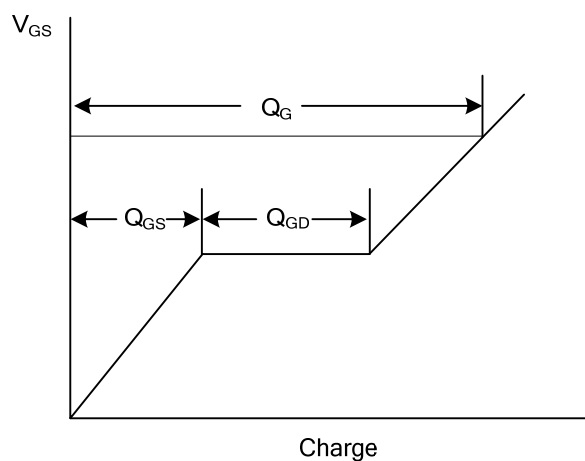
Switching Test Circuit



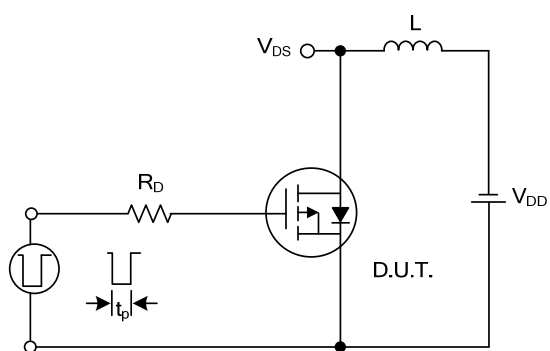
Switching Waveforms



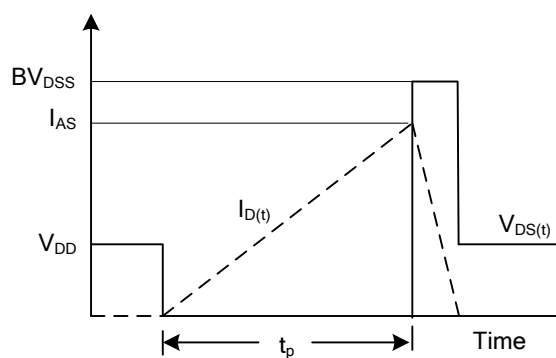
Gate Charge Test Circuit



Gate Charge Waveform

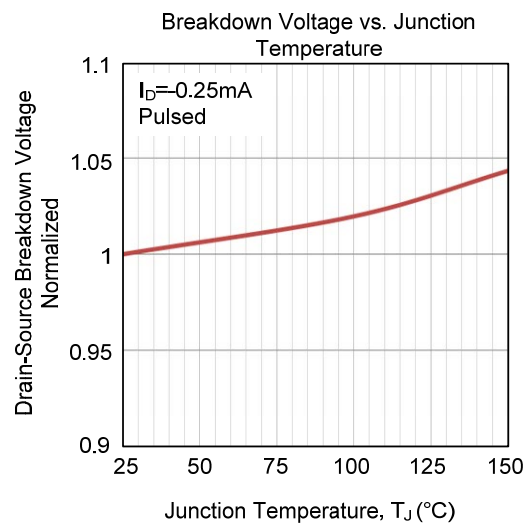
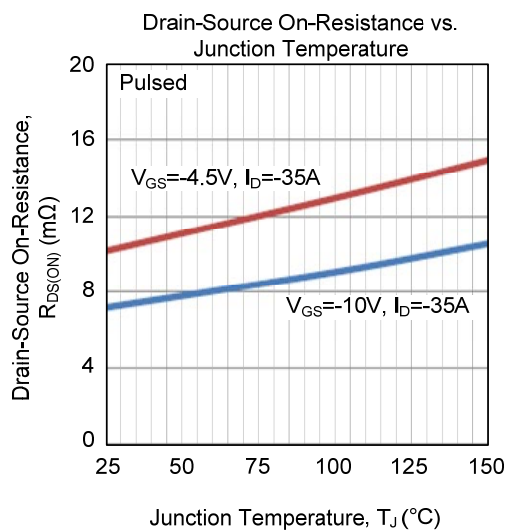
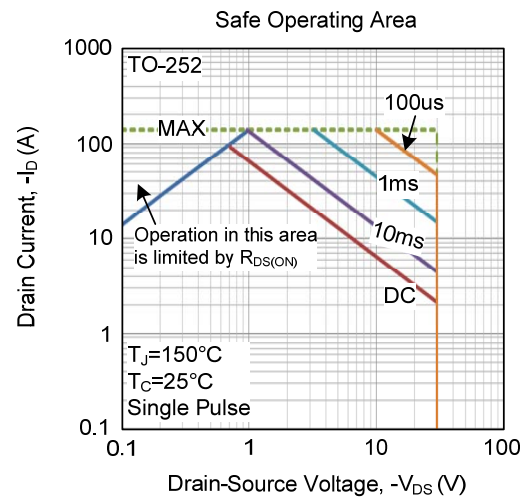
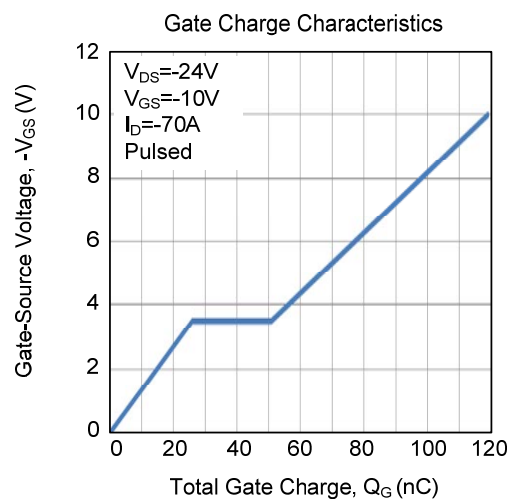
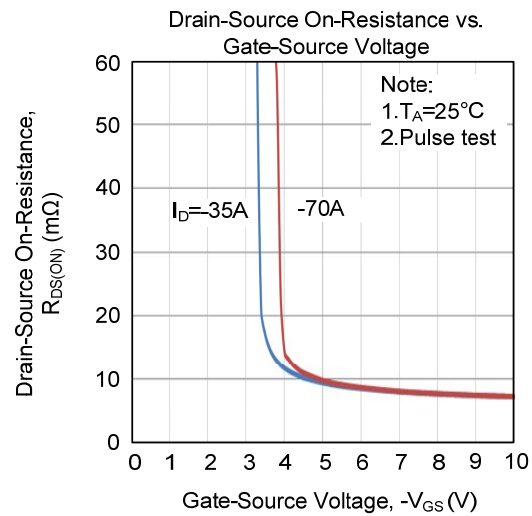
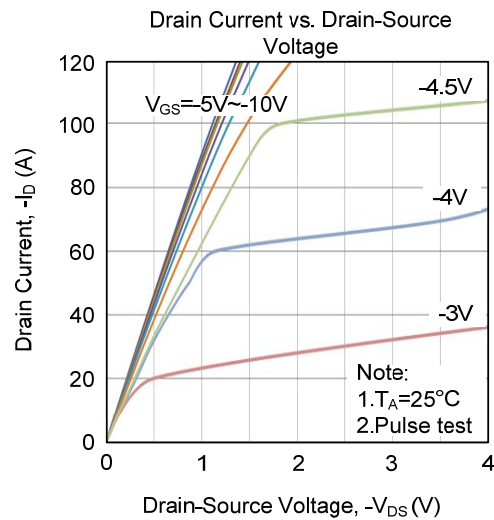


Unclamped Inductive Switching Test Circuit

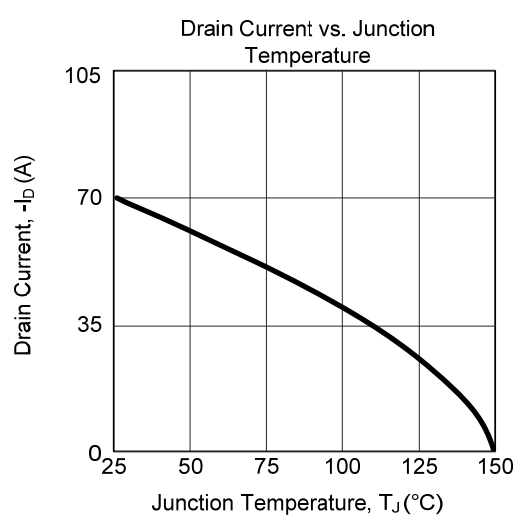
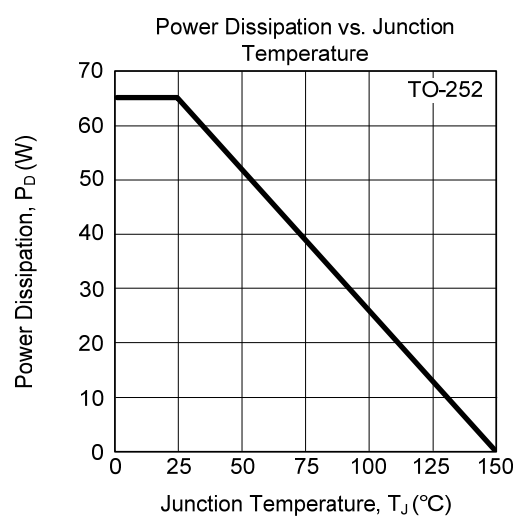
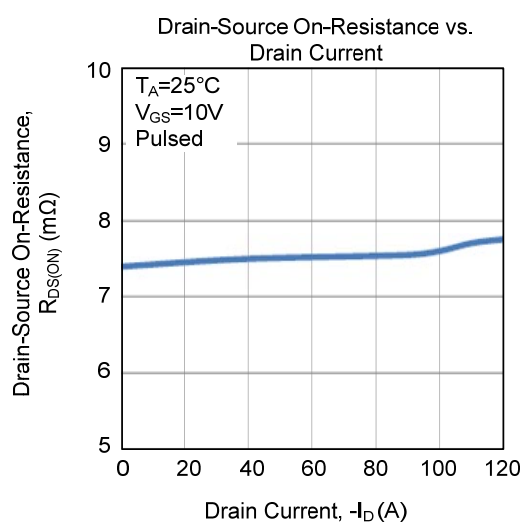
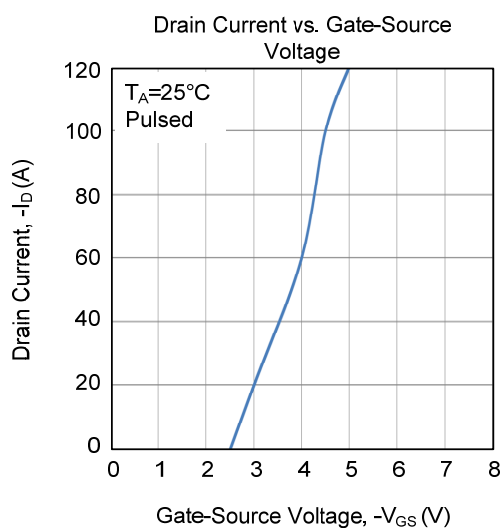
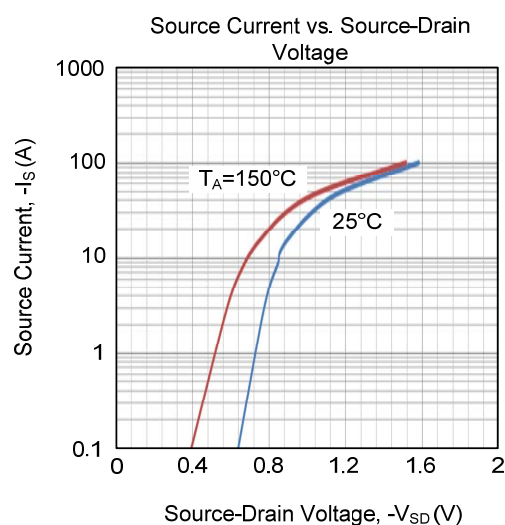
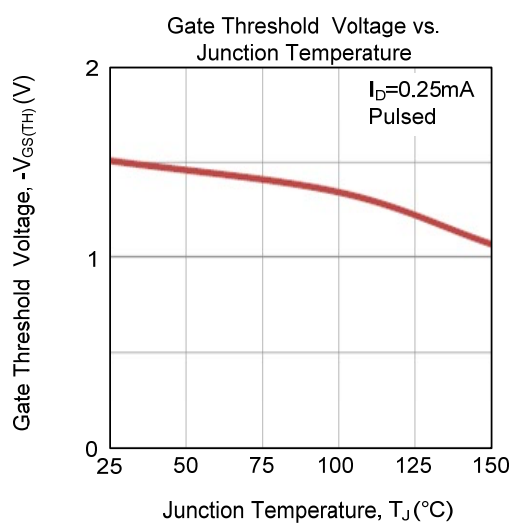


Unclamped Inductive Switching Waveforms

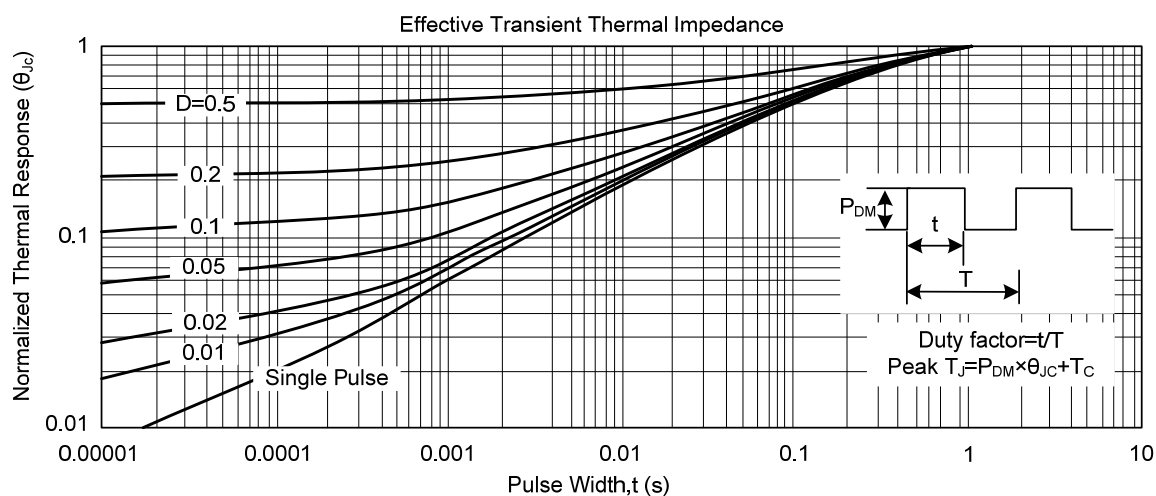
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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