



4N70K-MK

Power MOSFET

4A, 700V N-CHANNEL POWER MOSFET

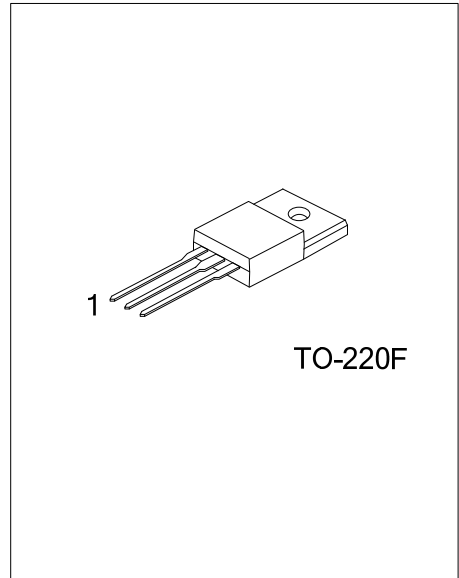
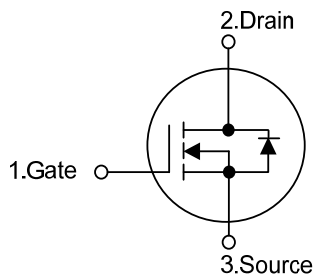
DESCRIPTION

The UTC **4N70K-MK** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche. This high speed switching power MOSFET is usually used in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} < 3.2\Omega$ @ $V_{GS} = 10V$
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL



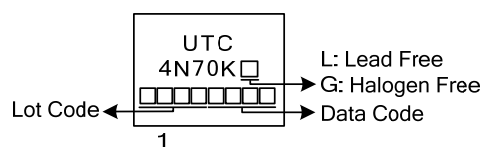
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
4N70KL-TF3-T	4N70KG-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

4N70KL-TF3-T (1) Packing Type (2) Package Type (3) Lead Free	(1) T: Tube (2) TF3: TO-220F (3) L: Lead Free, G: Halogen Free
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MARKING



■ **ABSOLUTE MAXIMUM RATINGS** ($T_A = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	700	V
Gate-Source Voltage		V _{GSS}	±30	V
Drain Current	Continuous	I _D	4	A
	Pulsed (Note 2)	I _{DM}	16	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	45	mJ
Power Dissipation		P _D	36	W
Derate above 25°C			0.288	W/°C
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Junction Temperature		T _J	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

3. $L = 5.7\text{mH}$, $I_{AS} = 4\text{ A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 4\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ **THERMAL DATA**

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	θ_{JC}	3.47	$^\circ\text{C}/\text{W}$

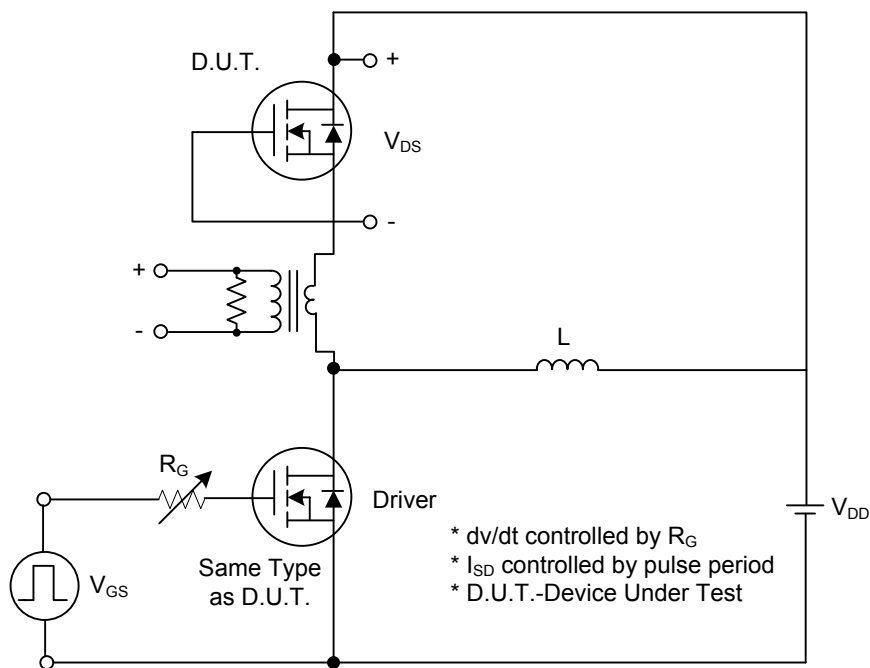
■ **ELECTRICAL CHARACTERISTICS** ($T_A = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250 μA	700			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 700 V, V _{GS} = 0 V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
	Reverse		V _{GS} = -30 V, V _{DS} = 0 V			-100	
Breakdown Voltage Temperature Coefficient		ΔBV _{DSS} /ΔT _J	I _D = 250μA, Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250 μA	2.5		4.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} = 10 V, I _D = 2.2 A			3.2	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1MHz		480	580	pF
Output Capacitance		C _{OSS}			45	80	pF
Reverse Transfer Capacitance		C _{RSS}			5	11	pF
SWITCHING CHARACTERISTICS							
Turn-On Delay Time		t _{D(ON)}	V _{DD} = 30V, I _D = 0.5A, R _G = 25Ω (Note 1, 2)		46		ns
Turn-On Rise Time		t _R			45		ns
Turn-Off Delay Time		t _{D(OFF)}			90		ns
Turn-Off Fall Time		t _F			33		ns
Total Gate Charge		Q _G	V _{DS} = 50V, I _D = 1.3A, V _{GS} = 10 V (Note 1, 2)		17.5	25	nC
Gate-Source Charge		Q _{GS}			6.2		nC
Gate-Drain Charge		Q _{GD}			3.0		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} = 0 V, I _S = 4 A			1.4	V
Maximum Continuous Drain-Source Diode Forward Current		I _S				4	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				17.6	A

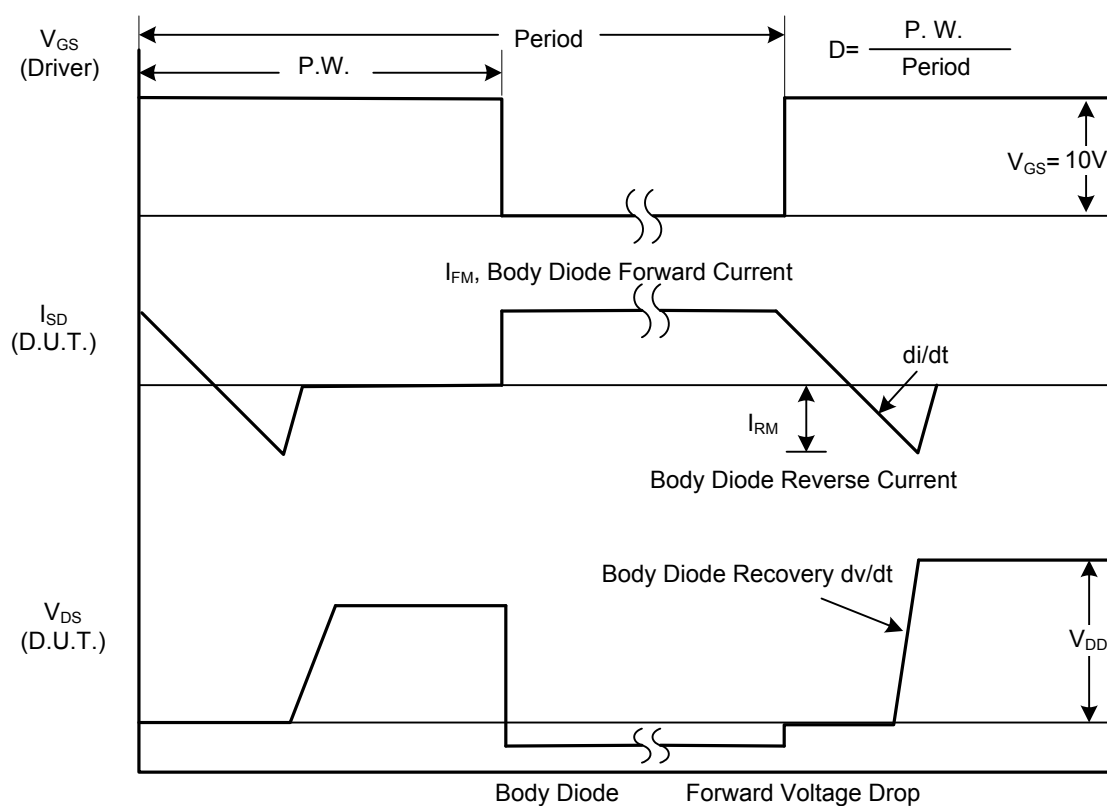
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

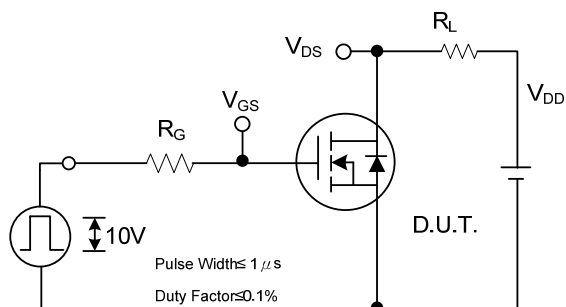


Peak Diode Recovery dv/dt Test Circuit

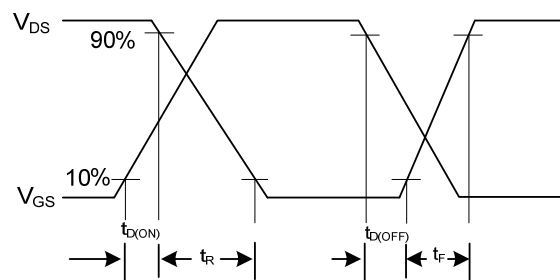


Peak Diode Recovery dv/dt Waveforms

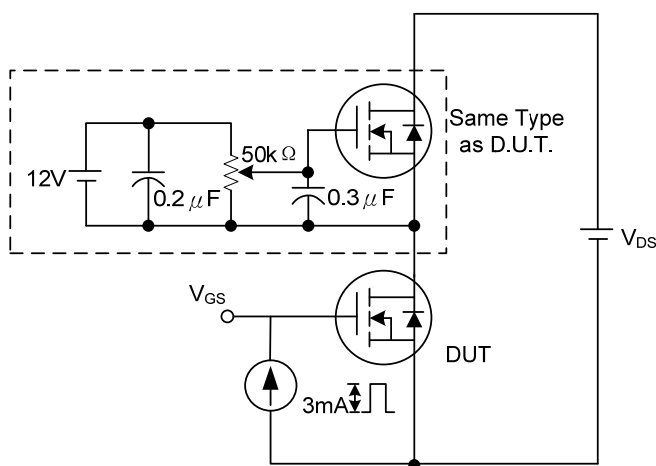
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



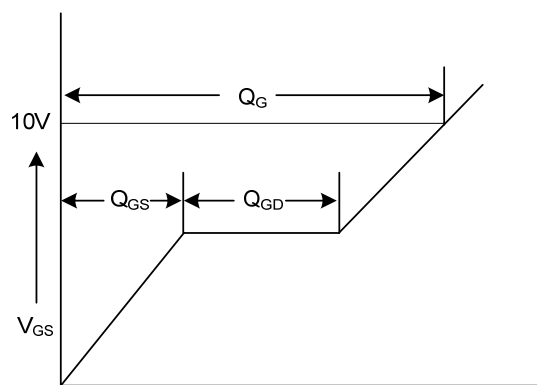
Switching Test Circuit



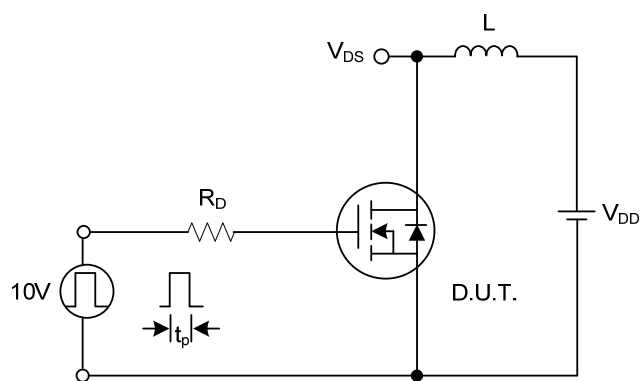
Switching Waveforms



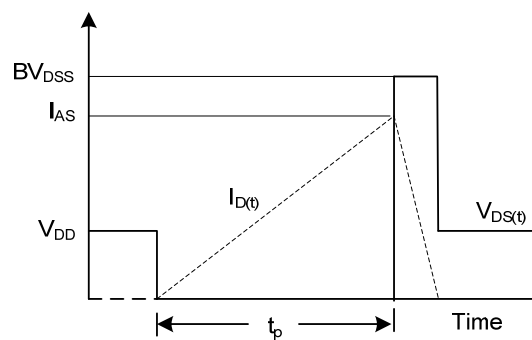
Gate Charge Test Circuit



Gate Charge Waveform

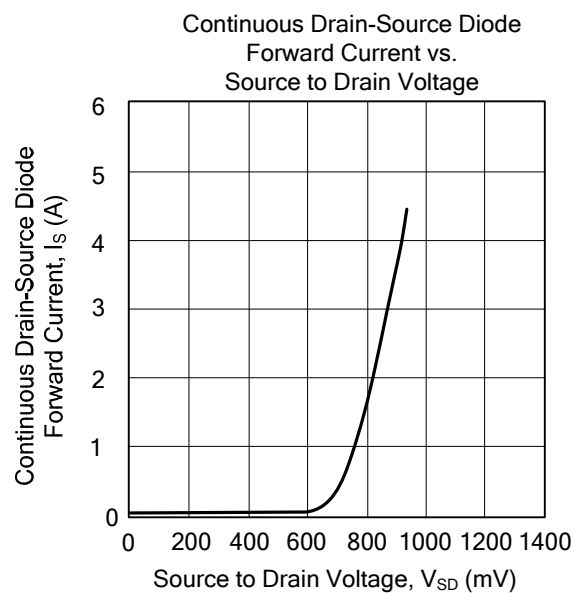
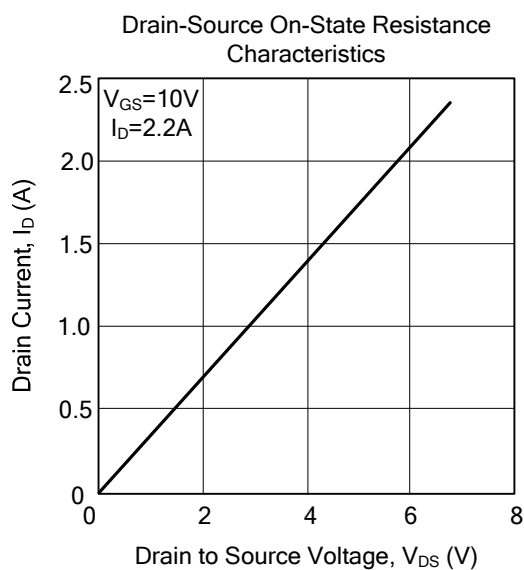
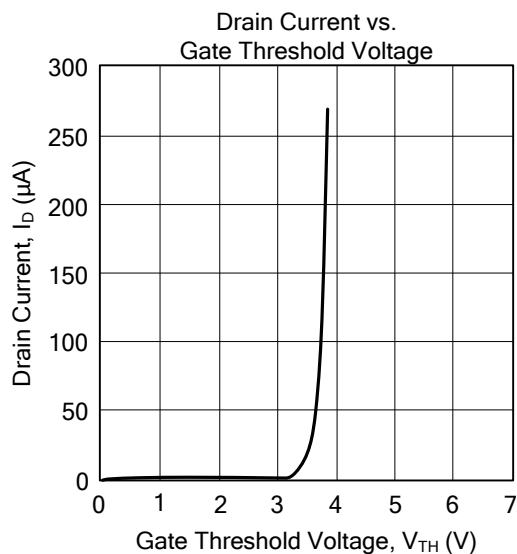
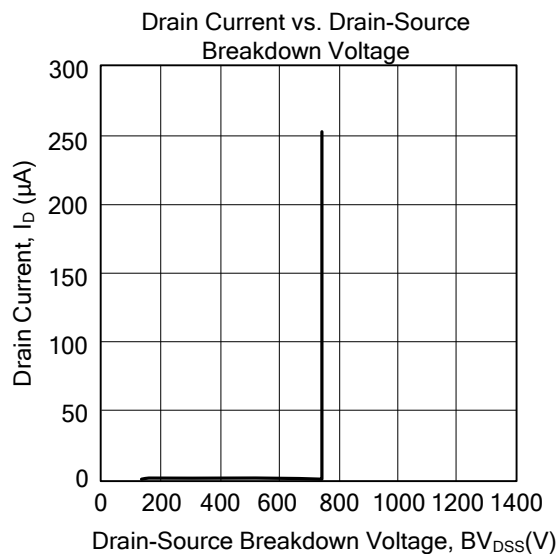


Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



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