



4NM80A

Preliminary

Power MOSFET

4A, 800V N-CHANNEL SUPER-JUNCTION MOSFET

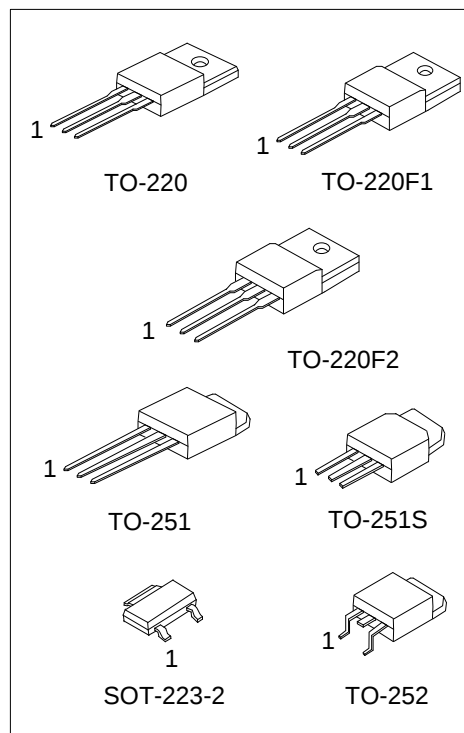
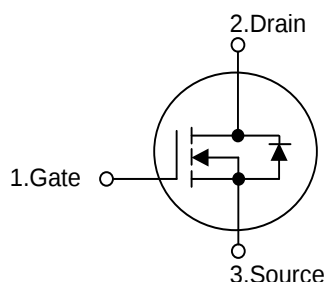
DESCRIPTION

The UTC 4NM80A is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)} \leq 2.04 \Omega$ @ $V_{GS} = 10V$, $I_D = 2.0A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL



ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
4NM80AL-AA2-R	4NM80AG-AA2-R	SOT-223-2	G	D	S	Tape Reel
4NM80AL-TA3-T	4NM80AG-TA3-T	TO-220	G	D	S	Tube
4NM80AL-TF1-T	4NM80AG-TF1-T	TO-220F1	G	D	S	Tube
4NM80AL-TF2-T	4NM80AG-TF2-T	TO-220F2	G	D	S	Tube
4NM80AL-TM3-T	4NM80AG-TM3-T	TO-251	G	D	S	Tube
4NM80AL-TMS-T	4NM80AG-TMS-T	TO-251S	G	D	S	Tube
4NM80AL-TN3-R	4NM80AG-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

4NM80AG-AA2-R	(1) T: Tube, R: Tape Reel (2) AA3: SOT-223-2, TA3: TO-220, TF1: TO-220F1 TF2: TO-220F2, TM3: TO-251, TMS: TO-251S TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

SOT-223-2	TO-220 / TO-220F / TO-220F1 TO-220F2 /TO-251 / TO-252
4NM80A 1 L: Lead Free G: Halogen Free Date Code	UTC 4NM80A 1 Lot Code L: Lead Free G: Halogen Free Date Code

■ **ABSOLUTE MAXIMUM RATINGS** ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	4.0	A
	Pulsed (Note 2)	I_{DM}	16	A
Avalanche Current (Note 2)		I_{AR}	1.5	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	179	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.0	V/ns
Power Dissipation	SOT-223-2	P_D	3	W
	TO-220		40	W
	TO-220F1/TO-220F2		23	W
	TO-251/TO-251S		24	W
	TO-252			
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L=159\text{mH}$, $I_{AS}=1.5\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD}\leq 4.0\text{A}$, $di/dt\leq 200\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ **THERMAL DATA**

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	SOT-223-2	θ_{JA}	150	$^\circ\text{C/W}$
	TO-220/TO-220F1		62.5	$^\circ\text{C/W}$
	TO-220F2			
	TO-251/TO-251S TO-252		110	$^\circ\text{C/W}$
Junction to Case	SOT-223-2	θ_{JC}	41.6 (Note)	$^\circ\text{C/W}$
	TO-220		3.13	$^\circ\text{C/W}$
	TO-220F1/TO-220F2		5.43	$^\circ\text{C/W}$
	TO-251/TO-251S		5.21 (Note)	$^\circ\text{C/W}$
	TO-252			

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

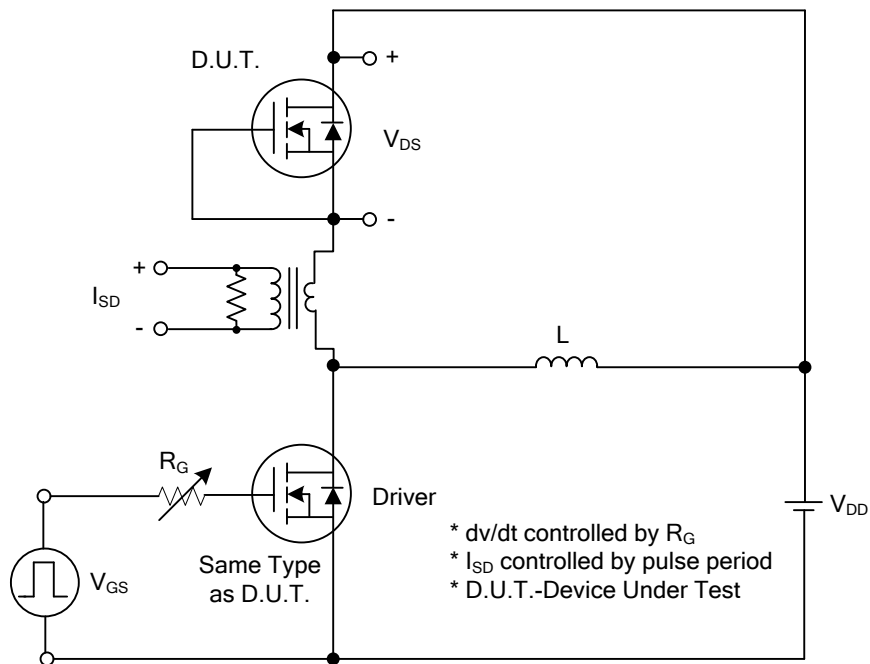
ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0V, I _D = 250μA	800			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 800V, V _{GS} = 0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} = 30V, V _{DS} = 0V			100	nA
	Reverse		V _{GS} = -30V, V _{DS} = 0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250μA	2.5		4.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} = 10V, I _D = 2.0A			2.04	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		320		pF
Output Capacitance		C _{OSS}			135		pF
Reverse Transfer Capacitance		C _{RSS}			9		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =50V, I _D =1.3A, I _G =100μA V _{GS} =10V (Note 1,2)		41		nC
Gate to Source Charge		Q _{GS}			4		nC
Gate to Drain Charge		Q _{GD}			9.5		nC
Turn-ON Delay Time (Note 1)		t _{D(ON)}	V _{DD} =30V, I _D =0.5A, R _G =25Ω, V _{GS} =10V (Note 1,2)		35		nS
Rise Time		t _R			55		nS
Turn-OFF Delay Time		t _{D(OFF)}			140		nS
Fall-Time		t _F			32		nS
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				4	A
Maximum Body-Diode Pulsed Current		I _{SM}				16	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =4.0A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t _{rr}	I _S =4.0A, V _{GS} =0V		340		nS
Body Diode Reverse Recovery Charge		Q _{rr}	dl _F /dt=100A/μs		2.7		μC

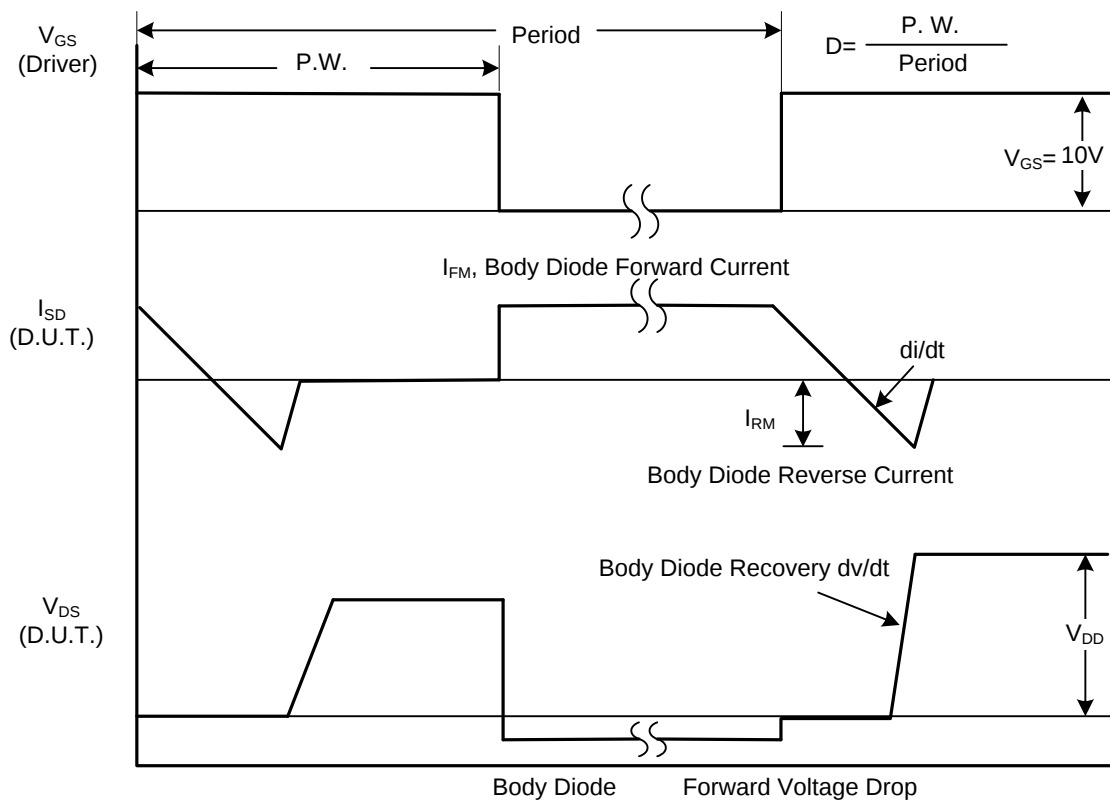
Notes: 1. Pulse Test : Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

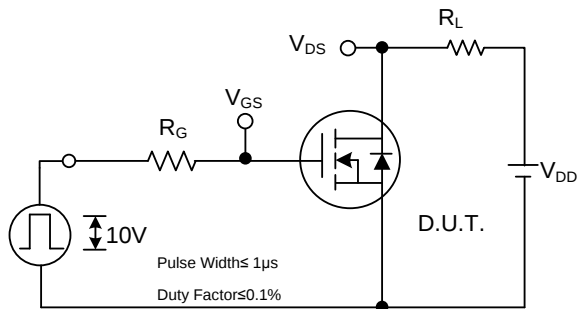


Peak Diode Recovery dv/dt Test Circuit

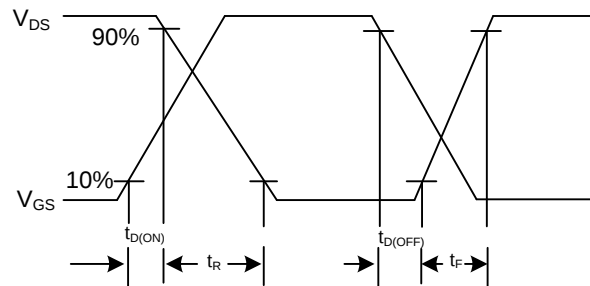


Peak Diode Recovery dv/dt Waveforms

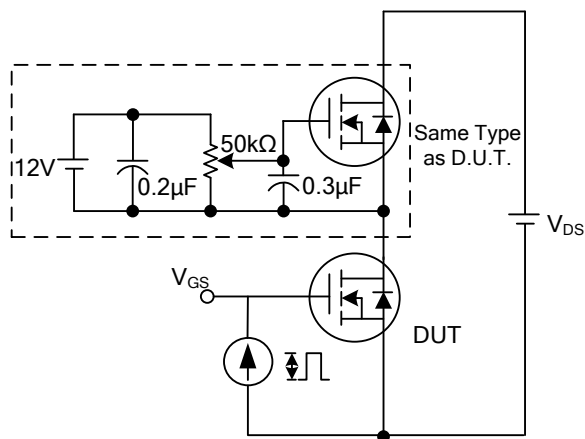
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



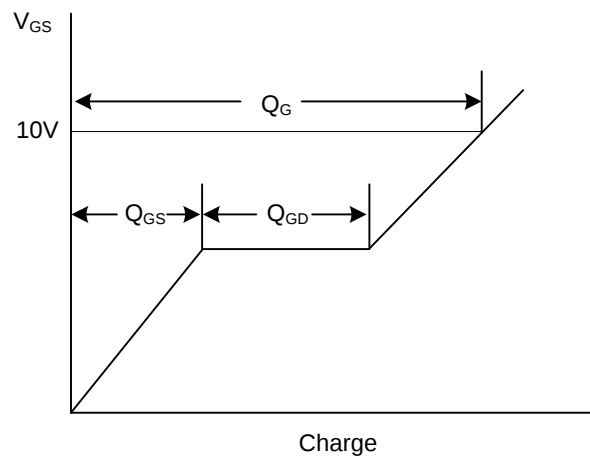
Switching Test Circuit



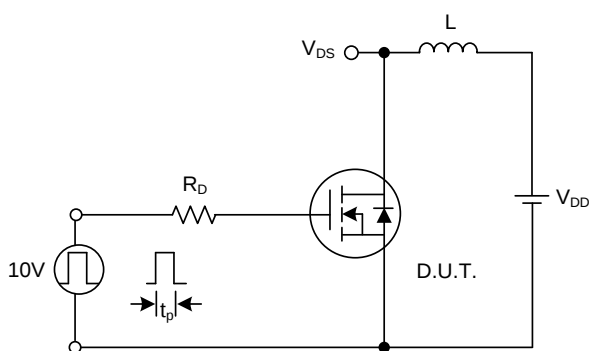
Switching Waveforms



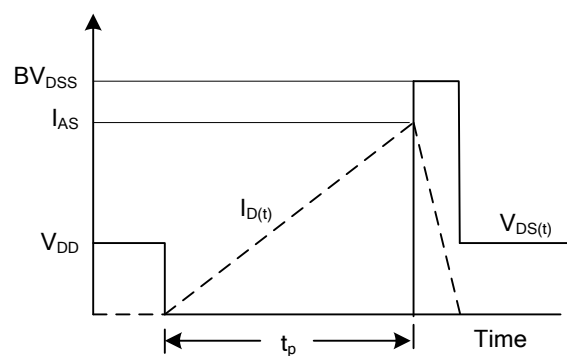
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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